Buchla Schematics	Front			Buchla Schematics	Rear	
1	1	+15V	**Left**	A	1	-15V
2	2	Q Gnd		B	2	NC (Q Gnd on BEMI 208)
3	3	NC (1973), EG end of cycle (1975)		C	3	NC (+5V on BEMI 208)
4	4	NC (N Gnd on BEMI 208)		D	4	NC (N Gnd on BEMI 208)
5	5	NC		E	5	NC
6	6	NC		F	6	NC
7	7	Sequencer stage 1 gateB out		H	7	Sequencer stage 2 gateB out
8	8	Sequencer stage 3 gateB out		J	8	Random 1B out
9	9	Sequencer stage 4 gateB out		K	9	Pulser period CV in
10	10	Sequencer stage 5 gateB out		L	10	MO index CV in
11	11	Sequencer num steps CV in		M	11	MO frequency CV in
12	12	Seq switches sumA gate out		N	12	Sequencer pulse in
13	13	Sequencer CVA out		P	13	CO pitch CV in
14	14	Random Voltage pulse in		R	14	Key pulseB out
15	15	Sequencer CVB out		S	15	Key CVB out
16	16	Random 2B out		T	16	Pulser pulse in
17	17	Env Gen pulse in		U	17	Timbre CV in
18	18	Pulser pulse out		V	18	Env Gen attack CV in
19	19	Env Gen duration CV in		W	19	Key pressure CVB out
20	20	Pulser CVB out		X	20	Env Gen CVB out
21	21	Env Gen decay CV in		Y	21	Seq switches sumB gate out
22	22	LPG1 level CV in		Z	22	MO modulation switch CV in
23	23	MO waveshape CV in		A-0	23	To Prog (inverter section)
24	24	CO & MO key in/out switch CV in		B-0	24	CO waveshape CV in
25	25	LPG2 level CV in		C-0	25	CO waveshape switch CV in
26	26	To Prog (detector section)		D-0	26	LPG1 mode switch CV in
27	27	LPG2 mode switch CV in		E-0	27	B Enable (+13.5V)
28	28	From Prog (inverter section)*	**Right**	F-0	28	LPG2 routing switch CV in

* requires input resistor

Dave Brown Aug 30, 2023