Buchla Schematics	Front	V2	V1 PCB1 Pin No.	V1
1	1	+15V		
2	2	Q Gnd		
3	3	NC (1973), EG end of cycle (1975)		
4	4	NC (N Gnd on BEMI 208)	43	13.5V
5	5	NC	41	Sequencer Stage 4 out
6	6	NC	39	Sequencer Stage 2 out
7	7	Sequencer stage 1 gateB out	37	Sequencer CV in*
8	8	Sequencer stage 3 gateB out	35	Sequencer CV in*
9	9	Sequencer stage 4 gateB out	33	Sequencer CV in*
10	10	Sequencer stage 5 gateB out	31	EG Duration CV in
11	11	Sequencer num steps CV in	29	Pulser CV in
12	12	Seq switches sumA gate out	27	Mod Index CV in
13	13	Sequencer CVA out	25	MO frequency CV in
14	14	Random Voltage pulse in	23	CO CV in (thru +/- switch)
15	15	Sequencer CVB out	21	Waveshape CV in
16	16	Random 2B out	19	Mod Index banana jack
17	17	Env Gen pulse in	17	CO CV banana jack
18	18	Pulser pulse out	15	LPG1 CV banana jack
19	19	Env Gen duration CV in	13	Timbre CV in
20	20	Pulser CVB out	11	NC
21	21	Env Gen decay CV in	9	NC
22	22	LPG1 level CV in	7	NC
23	23	MO waveshape CV in	5	NC
24	24	CO & MO key in/out switch CV in	3	LPG1 CV in
25	25	LPG2 level CV in	1	LPG2 CV in
26	26	To Prog (detector section)		
27	27	LPG2 mode switch CV in		
28	28	From Prog (inverter section)**		

Left

Right

Buchla Schematics	Rear	V2	V1 PCB1 Pin No.	V1
A	1	-15V		
B	2	NC (Q Gnd on BEMI 208)		
C	3	NC (+5V on BEMI 208)		
D	4	NC (N Gnd on BEMI 208)	44	Q Gnd
E	5	NC	42	Sequencer Stage 5 out
F	6	NC	40	Sequencer Stage 3 out
H	7	Sequencer stage 2 gateB out	38	Sequencer Stage 1 out
J	8	Random 1B out	36	Sequencer CV in*
K	9	Pulser period CV in	34	Sequencer CV in*
L	10	MO index CV in	32	EG Attack CV in
M	11	MO frequency CV in	30	EG Release CV in
N	12	Sequencer pulse in	28	Pulser CV in
P	13	CO pitch CV in	26	Mod Index CV in
R	14	Key pulseB out	24	MO frequency CV in
S	15	Key CVB out	22	CO CV in (always +)
T	16	Pulser pulse in	20	Pulser CV banana jack
U	17	Timbre CV in	18	MO CV banana jack
V	18	Env Gen attack CV in	16	Timbre CV banana jack
W	19	Key pressure CVB out	14	Timbre CV in
X	20	Env Gen CVB out	12	LPG1 CV in
Y	21	Seq switches sumB gate out	10	NC
Z	22	MO modulation switch CV in	8	NC
A-0	23	To Prog (inverter section)	6	NC
B-0	24	CO waveshape CV in	4	LPG2 CV banana jack
C-0	25	CO waveshape switch CV in	2	LPG2 CV in
D-0	26	LPG1 mode switch CV in		
E-0	27	B Enable (+13.5V)		
F-0	28	LPG2 routing switch CV in		

*I believe these were intended as separate stage CV offset inputs but are all wired in parallel

** Requires input resistor

Dave Brown Aug 30, 2023